

SERIAL NO: 09/847,168

GROUP: 21214

[illegible]

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO

CU	A1	C.T. Chiu et al., "VLSI Implementation of Real-Time Parallel DCT/DST Lattice Structures for Video Communications," University of Maryland, Technical Report 92-34, 1992
CU	A2	K.J.R. Liu et al., "Unified Parallel Lattice Structures for Time-Recursive Discrete Cosine/Sine/Hartley Transforms," University of Maryland, Technical Report TR 91-36, 1991
CU	A3	Teresa Chang et al., "Experiments in Real-Time Decoding of Layered Video," Proc SPIE Integration Issues in Large Commercial Media Delivery Systems, 2615, October 1995, p. 1-8

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.